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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	09/288,140
		Filing Date	April 8, 1999
		First Named Inventor	Shunpei YAMAZAKI et al.
		Group Art Unit	2871
		Examiner Name	M. Ton
Sheet 1 of 1	Attorney Docket Number	0756-1943	

U.S. PATENT DOCUMENTS						
Examiner Initials [*]	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
11		5,854,879		Inuzuka et al.	12/29/1998	

FOREIGN PATENT DOCUMENTS							
Examiner Initials [*]	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials [*]	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
M		Serikawa et al., <i>Low-Temperature Fabrication of High-Mobility Poly-Si TFTs for Large-Area LCD's</i> , Pages 137-141, 1989, Proceedings of the SID, Vol. 30/2.	
M		Wolf et al., <i>Silicon Procoessing for the VLSI Era</i> , Lattice Press, Volume 1, 1986, Pages 151-154.	

Examiner Signature	Date Considered	06/26/00
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.